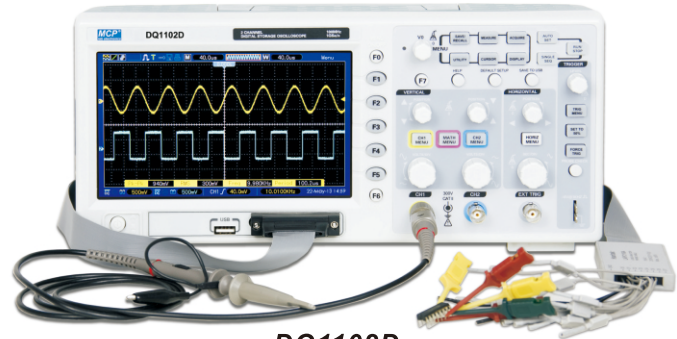


DQ1000D SERIES



Features

- Support logic analyzer and oscilloscope
- 1GSa/s sampling rate and 50GSa/s equivalent sampling rate
- 1024k recording length
- 7" wide screen 64k color TFT display
- USB-host for save and update



DQ1102D

Technical Data		DQ1062D	DQ1102D	DQ1202D
Channels		2 channels oscilloscope+ 16 channels logical analyser		
Sampling rate		1GSa/s		
Equivalent sampling rate		25GSa/s		
Display	Type	7" wide screen 64k color TFT LCD		
	Display resolution	800 horizontal × 480 vertical pixels		
	Display contrast	Adjustable (16 gears) with the progress bar		
	Sensitivity	2mV/div~5V/div		
	Vertical resolution	8 bit		
Vertical system	Width of band (-3dB)	DC (AC 10Hz) ~ 60MHz	DC (AC 10Hz) ~ 100MHz	DC (AC 10Hz) ~ 200MHz
	Selectable analog bandwidth limit	20MHz		
	Rise time	≤5.8ns	≤3.5ns	≤1.8ns
	DC gain Accuracy	±4%(2mV/div~5mV/div) ±3%(10mV/div~5V/div)		
	DC measurement accuracy	±(3%Rdg.+0.1div+1mV)(10mV/div~5V) ±(3%(Rdg.+vertical position)+0.2div+1%(vertical position) (2mV/div~200mV/div, +2mV; 200mV/div~5V/div, +50mV)		
Horizontal system	SEC/DIV range	2ns~40s/div, at 2-4-8 increment		
	Waveform interpolation	Sin(x)/x		
	Recording length	1024k		
	Sampling rate and delay time accuracy	±50ppm (any time interval ≥1ms)		
	Delta time measurement accuracy	Single: ±(1 sampling time interval + 100ppm×Rdg. + 0.6ns) Average values: ±(1 sampling time interval + 100ppm×Rdg. + 0.4ns)		
Trigger system	Mode	Auto, normal, single		
	Type	Edge, pulse, video, alternate, slope, over time		
	Hold off range	100ns~10s		
Math		+, -, ×, ÷		
		FFT		
Acquire input	Input coupling	DC, GND, AC		
	Input impedance	1MΩ ±2%, 20pF ±3pF		
	Probe attenuation	1×, 10×, 100×, 1000×		
	Max. input voltage	300V (DC+AC peak)		
Measurement		Voltage difference (ΔV) between cursors		
	Cursor	Time difference (ΔT) between cursors		
		Reciprocal of ΔT in Hz (1/ΔT)		
	Auto-measure	Vrms, Vavg, Vp-p, Vmax, Vmin, Vtop, Vmid, Vamp, Period, Freq, Rise, Fall, +Width, -Width, +Duty, -Duty, Delay, FRF, FFR, LRR, LRF, LFR, LFF		

DIGITAL STORAGE OSCILLOSCOPE

Technical Data		DQ1062D	DQ1102D	DQ1202D
I/O	Standard	USB(D), USB(H)		
	Options	LAN		
Calibrator	Output voltage	5V($\geq 1M\Omega$ load)		
	Output frequency	1kHz		
Logical analyzer	Input channel	D0~D15		
	Max. input impedance	200k (C=10P)		
	Max. sampling rate	500MHz		
	Recording length	512k		
	Max. input voltage	$\pm 60V$		
	Logic threshold range	$\pm 8V$		
	Compatible input	TTL, CMOS, ECL		
	Cursors	Voltage difference (ΔV) between cursors Time difference (ΔT) between cursors Reciprocal of ΔT in Hz ($1/\Delta T$)		
	Measurement	Period and Frequency		
	Record position	RefA RefB		
Trigger mode	Edge	D0~D15 select slope (rising or falling edge)		
	Pulse width	D0~D15 select pulse polarity (positive or negative pulse), trigger when ($=, \neq, >, <$), trigger pulse width		
	Code-type	D0~D15 select code-type (H, L, X)		
	Duration	D0~D15 select persist time and trigger when (data terminate, data start, and data delay)		
	Queue	D0~D15 select specific data index (0~3) and code-type (H, L, X)		
	Repeat	D0~D15 select code-type (H, L, X) and repeat times		
Power source		100~120VACrms ($\pm 10\%$), 45~440Hz; 30VA Max; CAT II		
		120~240VACrms ($\pm 10\%$), 45~66Hz; 30VA Max; CAT II		
Dimensions (W×H×D)	315×142×110mm			
Weight	2.1kg			
Accessories	Operation manual, power cord, USB cable, probe×2, software CD-ROM, logic analyzer probe			