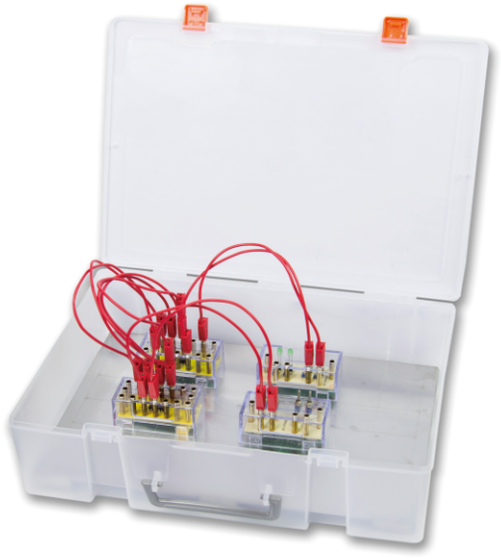
BASIC LOGIC CIRCUIT TRAINING SYSTEM F1-3

Features

The training system is used in the digital electronic circuits, it can be completed according to the F1-3 experiment manual. Totally 26 recommend basic logic circuit experiments are contained in this system with the corresponding components and more experiments can be designed to do by yourself.



System contain:

1. Gird panel and plastic box	1 pcs
2. Components	39 pcs
3. Leads	20 pcs
4. Experiment manual	1 pcs

EXPERIMENTS CONTENT

I Adder and Subtractor
1 Half-Adder, Half-Subtractor
2 SUM in Full-Adder and DIFFERENCE in Full-Subtractor
3 Co for X+Y+ Ci
4 Full-Adder with Half-Adders
5 2-Bit Parallel Binary Adder
6 4-Bit Binary Full-Adder/2's-complement 4-Bit Binary Full-Subtractor

II Bistable or Flip-Flop
1 R-S Flip-Flop with NAND Gates
2 Gated R-S Flip-Flop
3 D Flip-Flop
4 AND-Gated J-K Master-Slave Flip-Flop

III Binary Counters
1 Binary ripple counter
2 Synchronous counter

IV Divide-by-N Counters and Decade Counters
1 Modulus 3 Counter
2 Modulus 6 Counter
3 Decade Counter 2421
4 Decade Counter 8421
5 IC Decade Counter
6 IC Divide-by-10 Counter

V Shift Registers and Ring Counter
1 Shift Register
2 IC Shift Register
3 Quinary ring counter
4 Twisted-ring or Johnson Counter

VI Pulse Forming and Shaping/The Schmitt Trigger
1 Transistor Astable
2 IC Astable
3 Pulse Stretchers
4 Schmitt Trigger